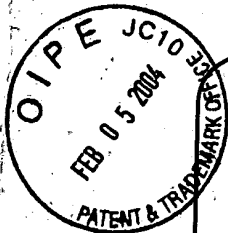


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Application Number	10/066,456
Filing Date	1/31/02
First Named Inventor	Steven Teig, et al
Group Art Unit	2825
Examiner Name	B. Tat
Attorney Docket Number	SPLX.P0099

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Or Individual name	Gregory Suh, Reg. No. 48,187, of Stattler Johansen & Adeli LLP
Signature	
Date	February 3, 2004

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Gregory Sun
Gregory Sun

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig, et al.

Serial No.: 10/066,456

Filing Date: 1/31/02

For: METHOD AND APPARATUS FOR
PERFORMING TECHNOLOGY
MAPPING

INFORMATION DISCLOSURE
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P.O. Box 1450
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Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit with this Information Disclosure Statement the attached Form PTO-1449, and copies of the documents listed in the 1449 form for consideration by the Examiner. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

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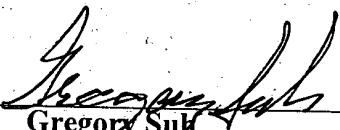
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Respectfully submitted,

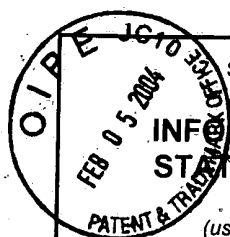
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of	2
Application Number		10/066,456	
Filing Date		1/31/02	
First Named Inventor		Steven Teig, et al.	
Group Art Unit		2825	
Examiner Name		B. Tat	
Attorney Docket Number		SPLX.P0099	

U.S. PATENT DOCUMENTS

Examiner* Initials	Cite No.	U.S. Patent Document Number	Kind Code* (if known)	Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	2002/0157063	A1	10-24-2002	Besson	
	2.	2002/0178432	A1	11-28-2002	Kim et al.	
	3.	2003/0145288	A1	07-31-2003	Wang et al.	
	4.	4,703,435		10-27-1987	Darringer et al.	
	5.	5,003,487		03-26-1991	Drumm et al.	
	6.	5,537,330		07-16-1996	Damiano et al.	
	7.	5,892,678		04-06-1999	Tokunoh et al.	
	8.	5,903,466		05-11-1999	Beausang et al.	
	9.	5,991,524		11-23-1999	Belkhale et al.	
	10.	6,080,204		06-27-2000	Mendel	
	11.	6,311,317	B1	10-30-2001	Khoche et al.	
	12.	6,389,586	B1	05-14-2002	McElvain	
	13.	6,397,170	B1	05-28-2002	Dean et al.	
	14.	6,421,818	B1	07-16-2002	Dupenloup et al.	
	15.	6,446,240	B1	09-03-2002	Iyer et al.	
	16.	6,453,447	B1	09-17-2002	Gardner et al.	
	17.	6,470,486	B1	10-22-2002	Knapp	
	18.	6,519,754	B1	02-11-2003	McElvain et al.	
	19.	6,519,609	B1	02-11-2003	Touzet	
	20.	6,539,536	B1	03-25-2003	Singh et al.	
	21.	6,543,037	B1	04-01-2003	Limquenco et al.	

Examiner Signature		Date Considered	
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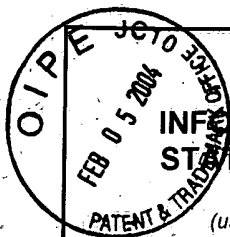
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Application Number				10/066,456	
Filing Date				1/31/02	
First Named Inventor				Steven Teig, et al.	
Group Art Unit				2825	
Examiner Name				B. Tat	
Attorney Docket Number				SPLX.P0099	
Sheet	2	of	2		

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22.	6,546,541	B1	04-08-2003	Petranovic et al.	
23.	6,618,835	B1	09-09-2003	Garlapati et al.	

NON PATENT LITERATURE DOCUMENTS

Examiner* Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
	24.	NN78055443, "Automatic Remap", IBM Technical Disclosure Bulletin, Vol. 20, No. 12, May 1978, pp. 5443-5445 (5 pages).	
	25.	NN9411291, "Design of Portable Library using Parameterized Cells", IBM Technical Disclosure Bulletin, Vol. 37, No. 11, pp. 291-292 (4 pages).	
	26.	Markovic et al., "FPGA to ASIC Conversion Design Methodology with the Support for Fast Retargeting to Different CMOS Implementation Technologies", Proceedings of 2002 22 nd International Conference on Microelectronics, Vol. 2, 14 May 2000, pp. 703-706.	
	27.	Ruiz et al., "Design and Prototyping of DSP Custom Circuits Based on a Library of Arithmetic Components", Vol. 1, 9 November 1997, pp. 191-196.	
	28.	Tamiya, Y., "Delay Estimation for Technology Independent Synthesis", 1997 Proceedings of the ASP-DAC '97, Asia and South Pacific Design Automation Conference, 28 January 1997, pp. 31-36.	

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